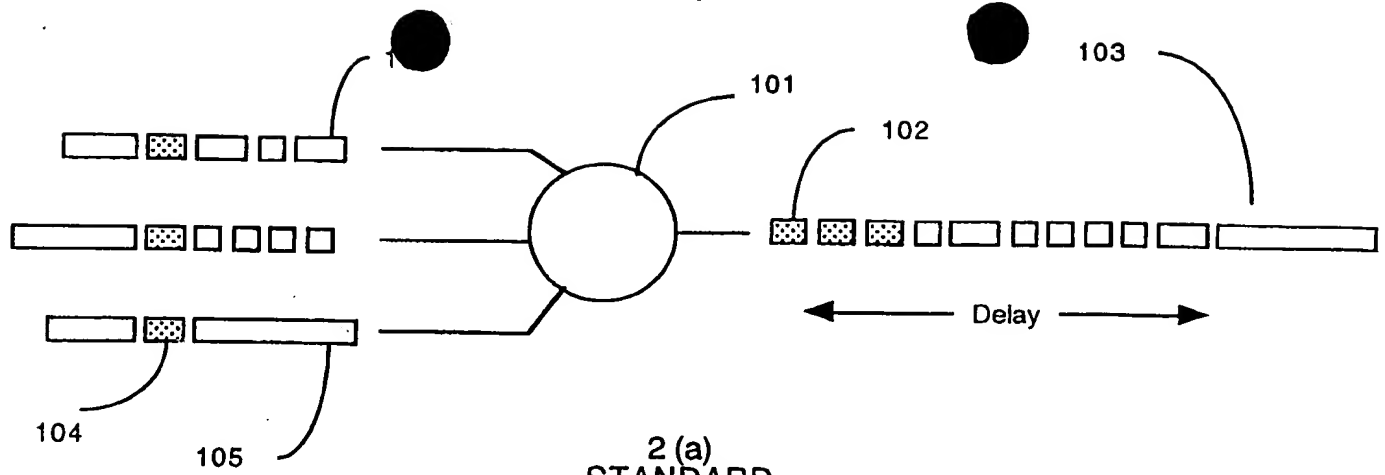
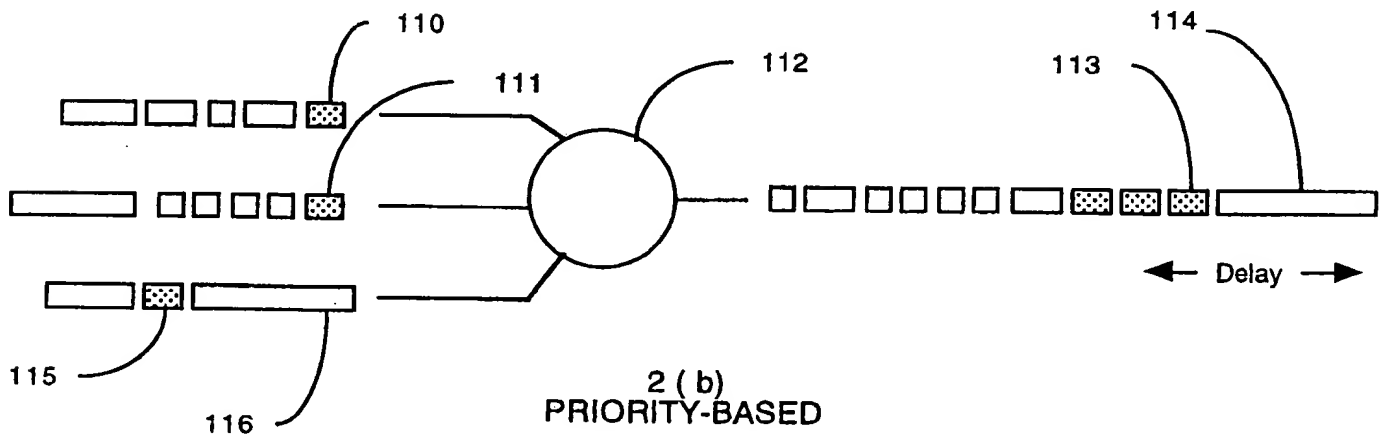
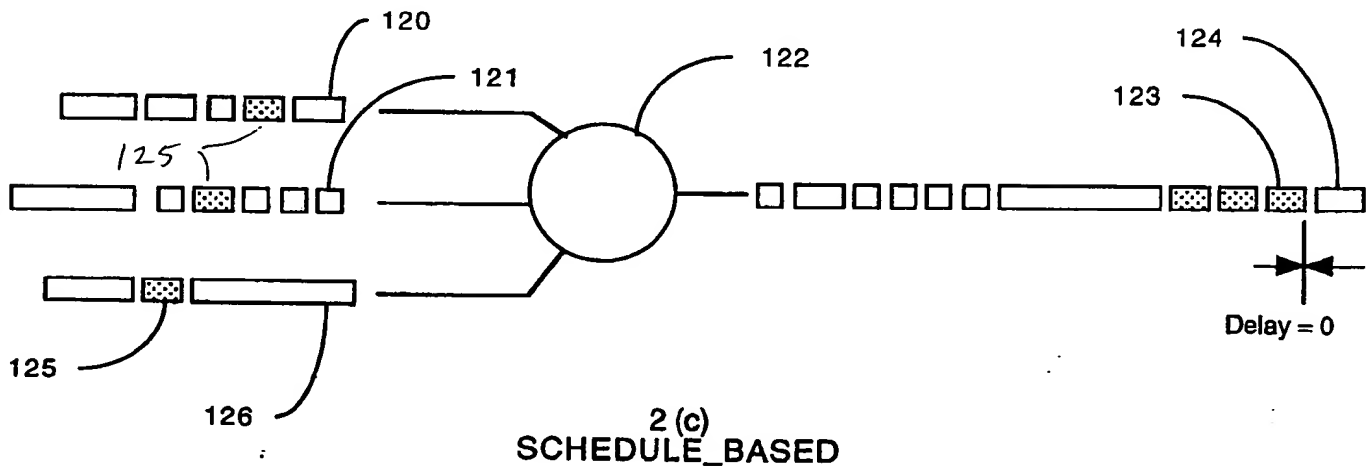


FIGURE 1  
SCHEDULED  
PACKET SWITCH  
DESIGN

2 (a)  
STANDARD2 (b)  
PRIORITY-BASED2 (c)  
SCHEDULE\_BASEDFIGURE 2  
PACKET SWITCHING  
METHODS

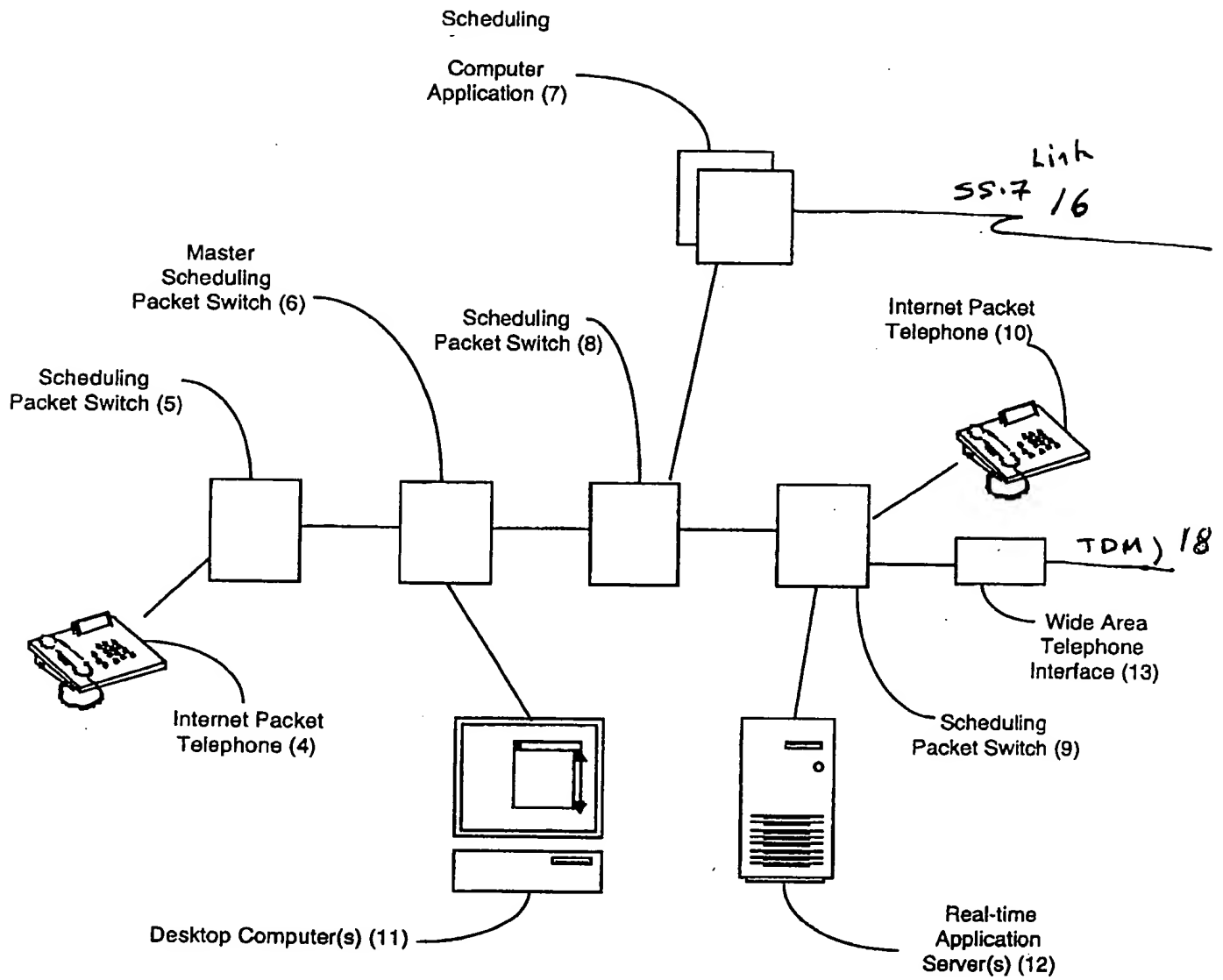


Figure 3  
Network of Scheduled Switches

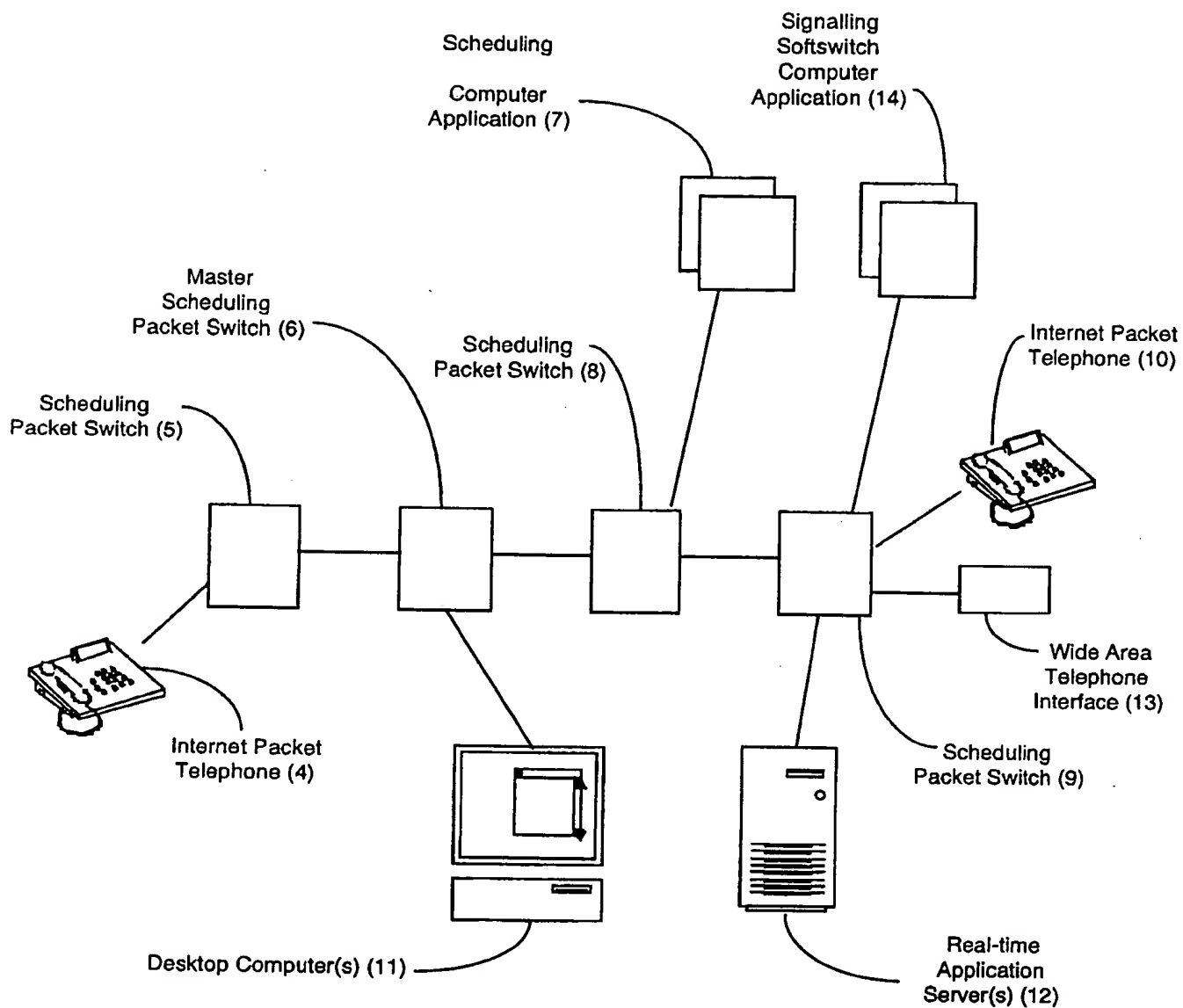


Figure 4  
Network of Scheduled Switches with Signalling Softswitch

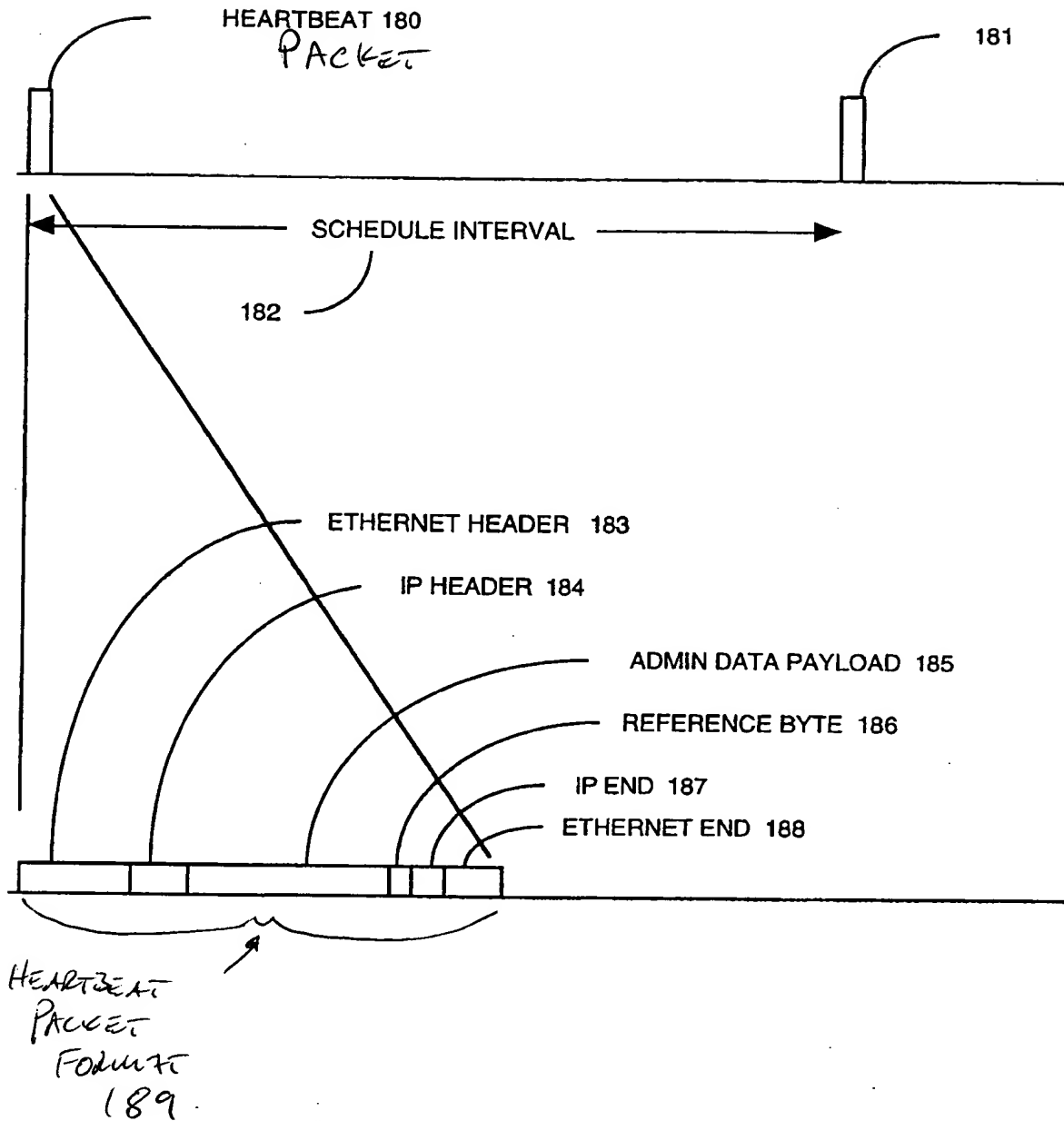


FIGURE 5  
HEARTBEAT PACKET  
STRUCTURE

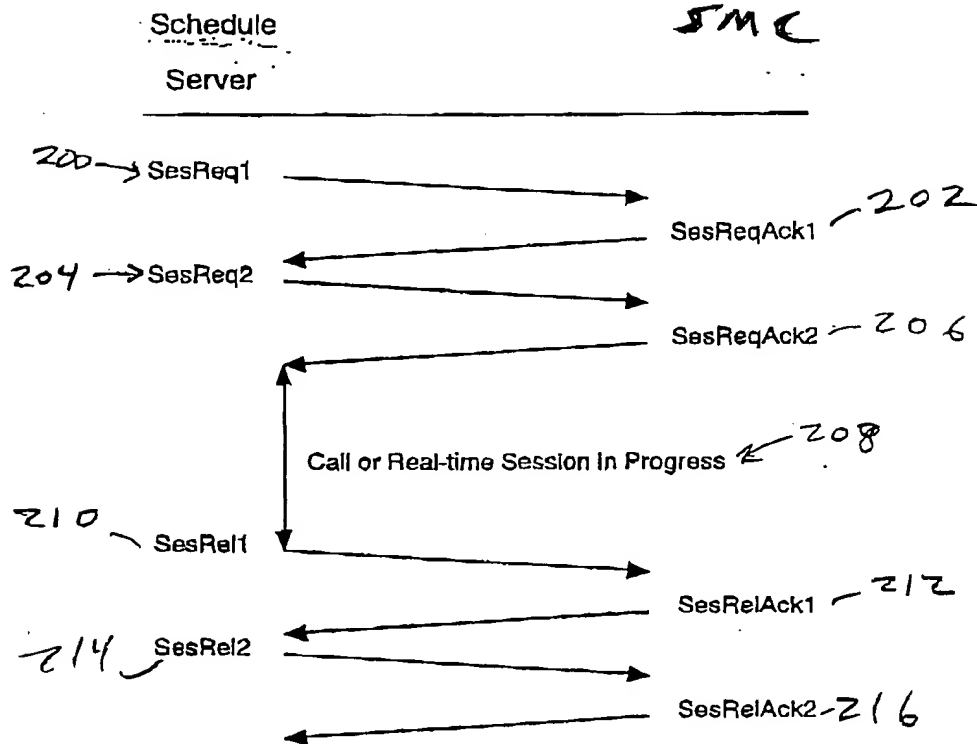


Figure 6  
Protocol Messages for a  
Bi-directional Real-time Session  
Establishment AND  
TERMINATION

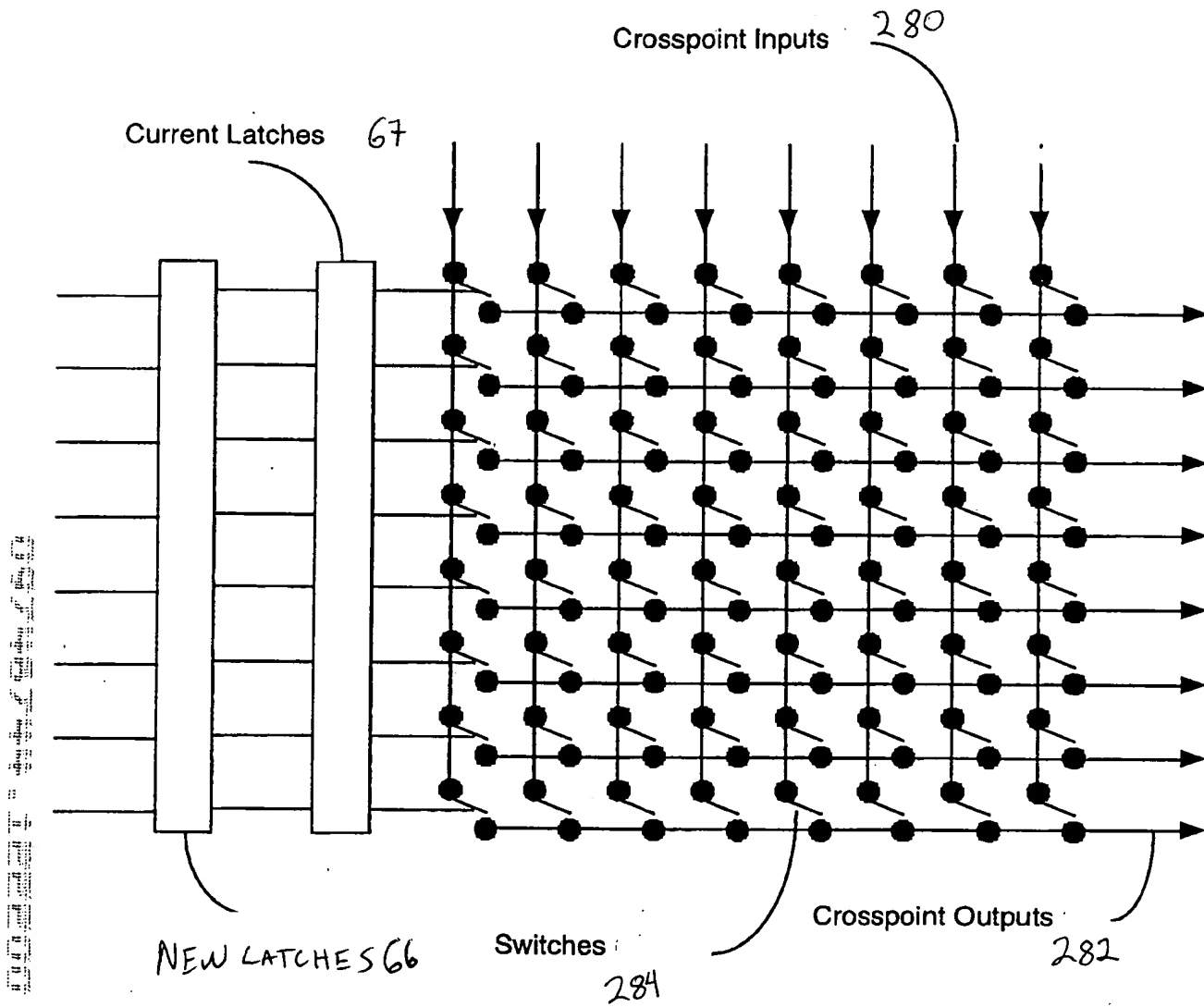
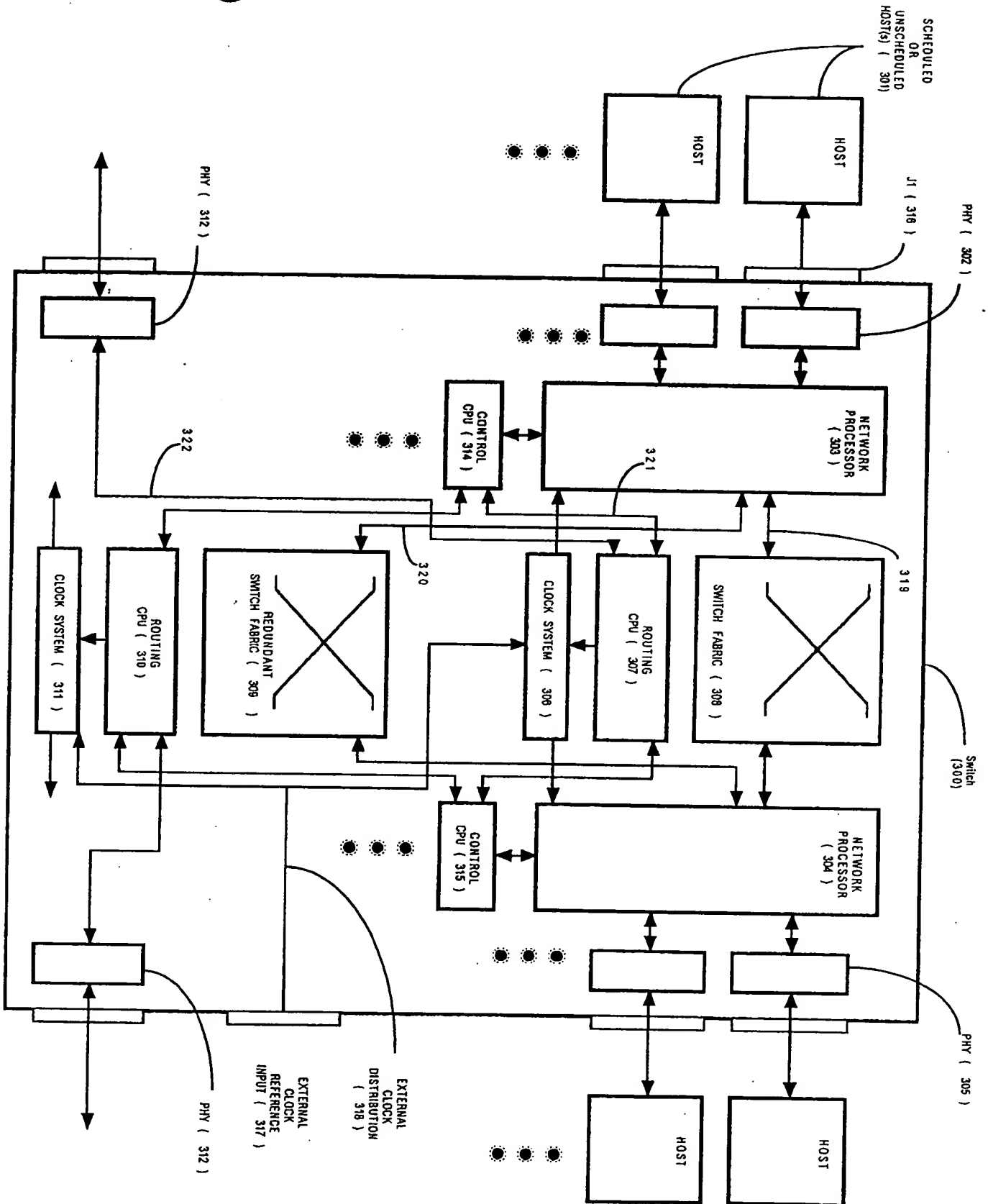


Figure 7  
Crosspoint Matrix



**FIGURE 8**  
**NETWORK PROCESSOR-BASED**  
**SCHEDULED PACKET SWITCH**



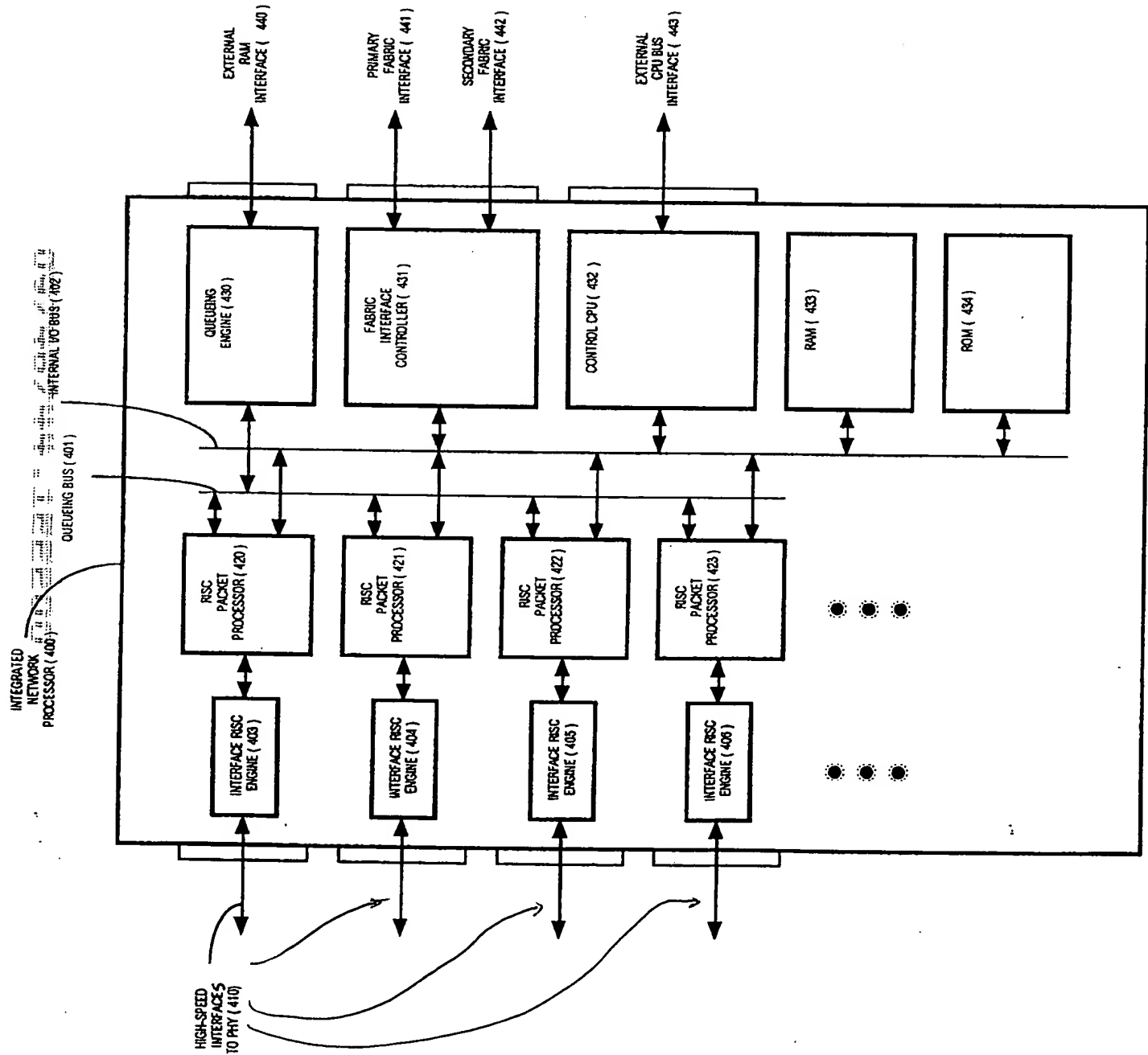


FIGURE 9  
TYPICAL  
NETWORK PROCESSOR  
EMBODIMENT

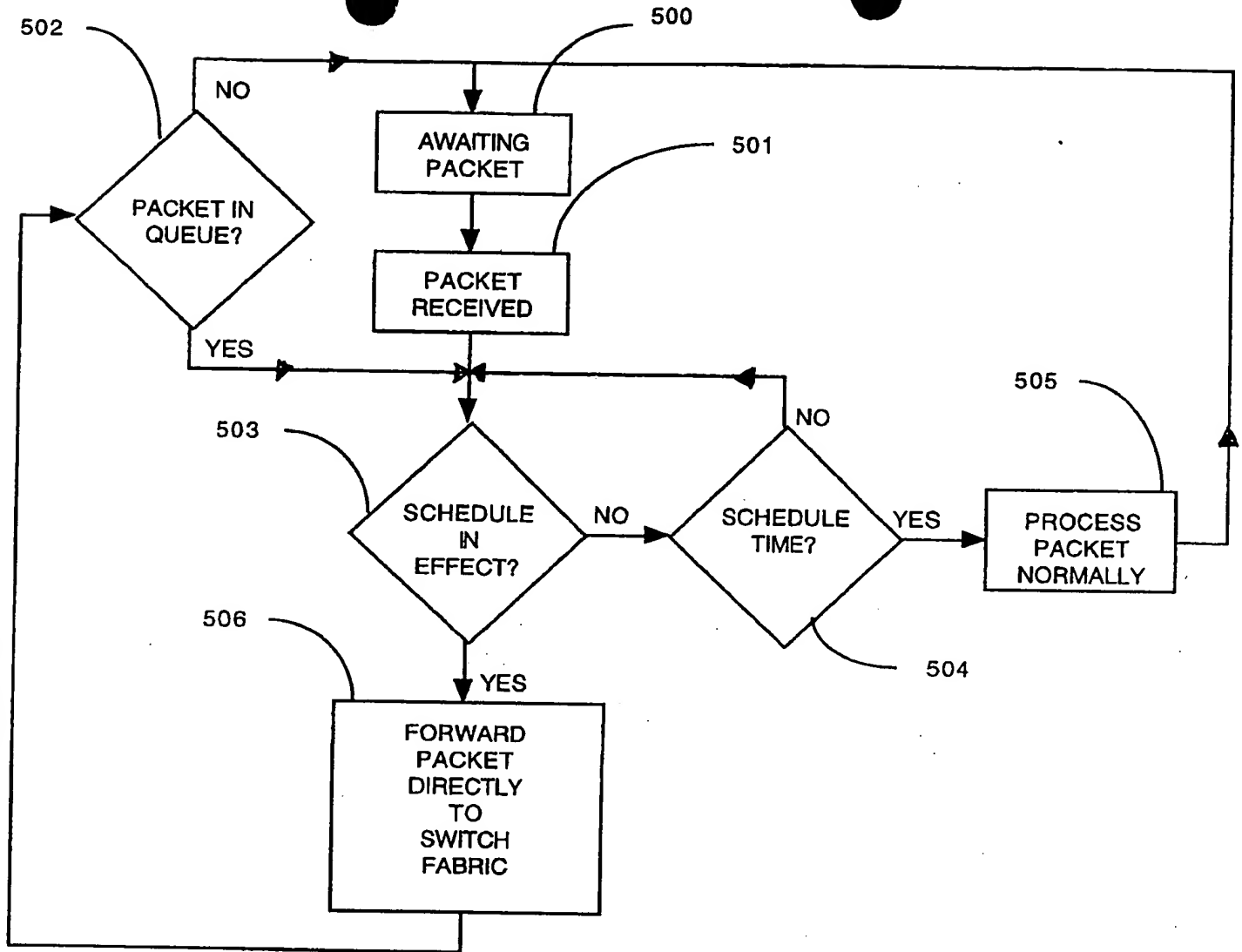


FIGURE 10  
RECEIVE SIDE  
PROCESSING

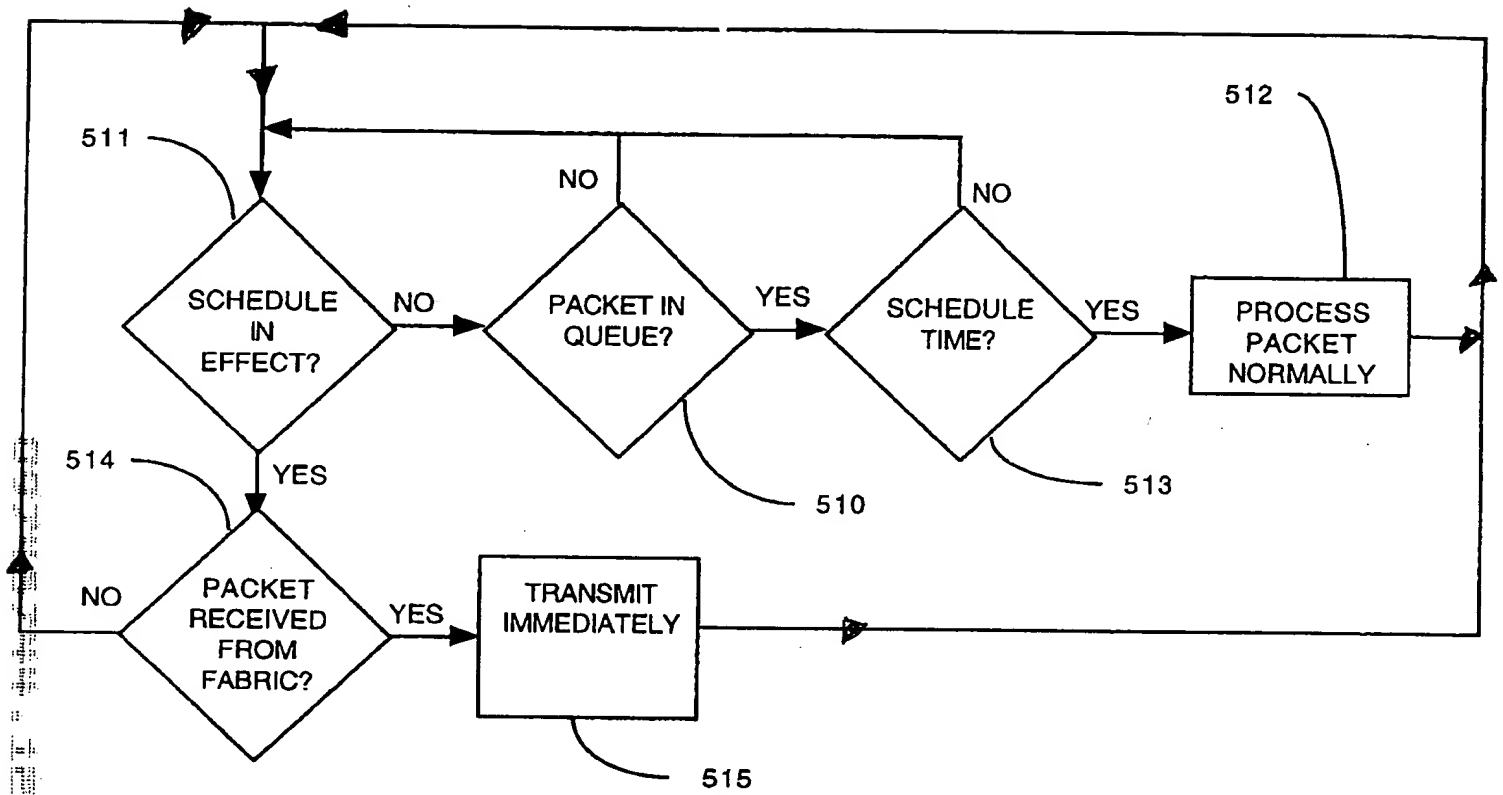


FIGURE 11  
TRANSMIT SIDE  
PROCESSING